

About the UCE (K.U):

University College of Engineering, Kakatiya University is a premier Government Technical Educational institution established in the year 1978 to impart the quality education to the engineering students in the eastern region of Telangana state, located at Kothagudem. Initially it was the constituent college of Osmania University, later shifted to KU with offering B. Tech. in Mining, CSE, EEE, ECE & IT, part time M. Tech. in Mining and Ph D. The campus spread over 390 acres with lot of greenery.

About the Course:

Hardware description languages (HDLs) are extremely important tools for modern digital designers. Once you have learned System Verilog or VHDL, you will be able to specify digital systems much faster than if you had to draw the complete schematics. The debug cycle is also often much faster, because modifications require code changes instead of tedious schematic rewiring. However, the debug cycle can be much longer using HDLs if you don't have a good idea of the hardware your code implies.

Who should attend:

Students and Faculty from ECE, EEE, CSE&IT Engineering background.

Registration:

Registration fees Rs.100/- (Through GPay/Phone Pay/Online) A/c No.52110435124, IFS Code.SBIN0020938

Use following link or scan the QR for Online registration

<https://forms.gle/hXgvz3Lfut8wfyzy7>

Last date of Registration: 22.12.2020

For further details Contact:

Sri. B. Srikanth, Coordinator, 9640388791(M)

- ✓ Expert from IIT.
- ✓ Participants will get E-Certificate. Separate Certificate will be issued to Faculty & Students.



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Asst. Professor of ECE, UCE, KU, Kothagudem

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A NATIONAL LEVEL TWO DAY WORKSHOP (ONLINE) ON

**HDL Coding, Simulation
and Career Opportunities in VLSI
(23-12-2020 to 24-12-2020)**



**Organized by
Department of ECE
University College of Engineering (KU)
Kakatiya University
Kothagudem**